

FORM Patent and Trademark Office-1449/A and B (Modified)		APPLICATION NO.: 09/579,646	ATTY. DOCKET NO.: A0312/7363
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		FILING DATE: May 26, 2000	
		APPLICANT: Katsufumi Nakamura et al.	
Sheet 1 of 1		GROUP ART UNIT: Not yet assigned	EXAMINER: Not yet assigned

U.S. PATENT DOCUMENTS

Examiner's Initials#	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
	1	5,757,230		Mangelsdorf	05-26-1998

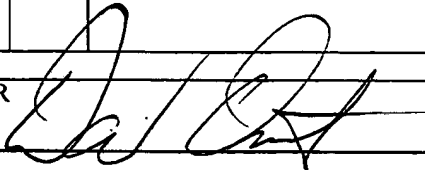
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FOREIGN PATENT DOCUMENTS

Examiner's Initials#	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/ Country	Number	Kind Code			

OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials#	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
	2	GREGORIAN and TEMES, "Analog MOS Integrated Circuits For Signal Processing," Wiley-Interscience, 1986, pp. 412-417	

EXAMINER 	DATE CONSIDERED 4/21/06
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#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. _____, filed _____, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

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